

AC784xx_DFP PBR
4.0.0

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Chapter 1

Class Index

1.1 Class List

Here are the classes, structs, unions and interfaces with brief descriptions:

Pbr_CfgType	Pbr configure structure	3
Pbr_MasterCfgType	Pbr master configure structure	4
Pbr_PeriphCfgType	Pbr peripheral configure structure	5

Chapter 2

File Index

2.1 File List

Here is a list of all files with brief descriptions:

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Chapter 3

Class Documentation

3.1 Pbr_CfgType Struct Reference

pbr configure structure

```
#include <Pbr_Hal.h>
```

Public Attributes

- uint8 [MasterCfgCnt](#)
- uint8 [PeriphCfgCnt](#)
- const [Pbr_MasterCfgType](#) * [MasterCfg](#)
- const [Pbr_PeriphCfgType](#) * [PeriphCfg](#)

3.1.1 Detailed Description

pbr configure structure

Definition at line 183 of file Pbr_Hal.h.

3.1.2 Member Data Documentation

3.1.2.1 MasterCfg

```
const Pbr\_MasterCfgType* Pbr_CfgType::MasterCfg
```

Definition at line 187 of file Pbr_Hal.h.

3.1.2.2 MasterCfgCnt

```
uint8 Pbr_CfgType::MasterCfgCnt
```

Definition at line 185 of file Pbr_Hal.h.

3.1.2.3 PeriphCfg

```
const Pbr_PeriphCfgType* Pbr_CfgType::PeriphCfg
```

Definition at line 188 of file Pbr_Hal.h.

3.1.2.4 PeriphCfgCnt

```
uint8 Pbr_CfgType::PeriphCfgCnt
```

Definition at line 186 of file Pbr_Hal.h.

The documentation for this struct was generated from the following file:

- [Pbr_Hal.h](#)

3.2 Pbr_MasterCfgType Struct Reference

pbr master configure structure

```
#include <Pbr_Hal.h>
```

Public Attributes

- uint8 [Value](#)
- [Pbr_MasterIdType](#) MasterId

3.2.1 Detailed Description

pbr master configure structure

Definition at line 165 of file Pbr_Hal.h.

3.2.2 Member Data Documentation

3.2.2.1 MasterId

[Pbr_MasterIdType](#) Pbr_MasterCfgType::MasterId

Definition at line 168 of file Pbr_Hal.h.

3.2.2.2 Value

uint8 Pbr_MasterCfgType::Value

Definition at line 167 of file Pbr_Hal.h.

The documentation for this struct was generated from the following file:

- [Pbr_Hal.h](#)

3.3 Pbr_PeriphCfgType Struct Reference

pbr peripheral configure structure

```
#include <Pbr_Hal.h>
```

Public Attributes

- uint8 [Value](#)
- [Pbr_PeriphIdType](#) PeriphId

3.3.1 Detailed Description

pbr peripheral configure structure

Definition at line 174 of file Pbr_Hal.h.

3.3.2 Member Data Documentation

3.3.2.1 PeriphId

[Pbr_PeriphIdType](#) Pbr_PeriphCfgType::PeriphId

Definition at line 177 of file Pbr_Hal.h.

3.3.2.2 Value

uint8 Pbr_PeriphCfgType::Value

Definition at line 176 of file Pbr_Hal.h.

The documentation for this struct was generated from the following file:

- [Pbr_Hal.h](#)

Chapter 4

File Documentation

4.1 AC784xx_API_Reference_Manual_PBR.pdf File Reference

4.2 AC784xx_Pbr_Reg.h File Reference

pbr access register inline function definition.

```
#include "Device_Register.h"
```

Macros

- #define [PBR_PACRA](#) (PBR_BASE + 0x10U)

Functions

- LOCAL_INLINE void [Pbr_Reg_SetPeriphAccess](#) (uint32 PeriphIdx, uint8 Value)
Set value to peripheral access permission configuration register.
- LOCAL_INLINE uint8 [Pbr_Reg_GetPeriphAccess](#) (uint32 PeriphIdx)
Get peripheral access permission configuration register value.
- LOCAL_INLINE void [Pbr_Reg_SetMasterPrivilege](#) (uint32 MasterIdx, uint8 Value)
Set master privilege configuration register value.
- LOCAL_INLINE uint8 [Pbr_Reg_GetMasterPrivilege](#) (uint32 MasterIdx)
Get master privilege configuration register value.

4.2.1 Detailed Description

pbr access register inline function definition.

4.2.2 Macro Definition Documentation

4.2.2.1 PBR_PACRA

```
#define PBR_PACRA (PBR_BASE + 0x10U)
```

Definition at line 66 of file AC784xx_Pbr_Reg.h.

4.2.3 Function Documentation

4.2.3.1 Pbr_Reg_GetMasterPrivilege()

```
LOCAL_INLINE uint8 Pbr_Reg_GetMasterPrivilege (
    uint32 MasterIdx )
```

Get master privilege configuration register value.

Note

Function ID: DES_PBR_API_013

Parameters

in	<i>MasterIdx</i>	master idx
----	------------------	------------

Returns

uint8: privilege register value

Definition at line 123 of file AC784xx_Pbr_Reg.h.

4.2.3.2 Pbr_Reg_GetPeriphAccess()

```
LOCAL_INLINE uint8 Pbr_Reg_GetPeriphAccess (
    uint32 PeriphIdx )
```

Get peripheral access permission configuration register value.

Note

Function ID: DES_PBR_API_011

Parameters

in	<i>PeriphIdx</i>	peripheral idx
in	<i>Value</i>	the value being set

Returns

uint8: access permission register value

Definition at line 93 of file AC784xx_Pbr_Reg.h.

4.2.3.3 Pbr_Reg_SetMasterPrivilege()

```
LOCAL_INLINE void Pbr_Reg_SetMasterPrivilege (  
    uint32 MasterIdx,  
    uint8 Value )
```

Set master privilege configuration register value.

Note

Function ID: DES_PBR_API_012

Parameters

in	<i>MasterIdx</i>	master idx
in	<i>Value</i>	the value being set

Returns

void

Definition at line 110 of file AC784xx_Pbr_Reg.h.

4.2.3.4 Pbr_Reg_SetPeriphAccess()

```
LOCAL_INLINE void Pbr_Reg_SetPeriphAccess (  
    uint32 PeriphIdx,  
    uint8 Value )
```

Set value to peripheral access permission configuration register.

Note

Function ID: DES_PBR_API_010

Parameters

in	<i>PeriphIdx</i>	peripheral idx
in	<i>Value</i>	the value being set

Returns

void

Definition at line 77 of file AC784xx_Pbr_Reg.h.

4.3 Pbr_Hal.c File Reference

pbr hal source file.

```
#include "Pbr_Hal.h"
#include "AC784xx_Pbr_Reg.h"
```

Functions

- void [Pbr_Hal_Init](#) (const [Pbr_CfgType](#) *Cfg)
Initialize pbr master and peripheral configuration.

4.3.1 Detailed Description

pbr hal source file.

4.3.2 Function Documentation

4.3.2.1 Pbr_Hal_Init()

```
void Pbr_Hal_Init (
    const Pbr\_CfgType * Cfg )
```

Initialize pbr master and peripheral configuration.

Note

Function ID: DES_MCL_API_301

Parameters

in	Cfg	the pointer to the Pbr_CfgType structure
----	-----	--

Returns

void

Definition at line 64 of file Pbr_Hal.c.

4.4 Pbr_Hal.h File Reference

pbr hal define.

```
#include "Device_Register.h"
```

Classes

- struct [Pbr_MasterCfgType](#)
pbr master configure structure
- struct [Pbr_PeriphCfgType](#)
pbr peripheral configure structure
- struct [Pbr_CfgType](#)
pbr configure structure

Enumerations

- enum [Pbr_MasterIdType](#) { [PBR_MASTER_CORE](#) = 0x0U, [PBR_MASTER_DEBUGGER](#), [PBR_MASTER_DMA](#), [PBR_MASTER_MAX](#) }
pbr master id
- enum [Pbr_PeriphIdType](#) { [PBR_PERIPH_PWM0](#) = 0U, [PBR_PERIPH_PWM1](#), [PBR_PERIPH_PWM2](#), [PBR_PERIPH_PWM3](#), [PBR_PERIPH_PWM4](#), [PBR_PERIPH_PWM5](#), [PBR_PERIPH_CRC](#) = 8U, [PBR_PERIPH_GPIO](#), [PBR_PERIPH_PDT0](#), [PBR_PERIPH_PDT1](#), [PBR_PERIPH_MPU](#), [PBR_PERIPH_EIM](#), [PBR_PERIPH_SMU](#), [PBR_PERIPH_CKGEN](#) = 32U, [PBR_PERIPH_RESERVE33](#), [PBR_PERIPH_FLASH](#), [PBR_PERIPH_ADC0](#), [PBR_PERIPH_ADC1](#), [PBR_PERIPH_ACOMP0](#), [PBR_PERIPH_SPM](#) = 45U, [PBR_PERIPH_RTC](#), [PBR_PERIPH_EIO](#), [PBR_PERIPH_WDG](#) = 48U, [PBR_PERIPH_EWDG](#), [PBR_PERIPH_SPI0](#), [PBR_PERIPH_SPI1](#), [PBR_PERIPH_SPI2](#), [PBR_PERIPH_I2C0](#), [PBR_PERIPH_TIMER](#) = 64U, [PBR_PERIPH_DMA](#), [PBR_PERIPH_UART0](#), [PBR_PERIPH_UART1](#), [PBR_PERIPH_UART2](#), [PBR_PERIPH_UART3](#), [PBR_PERIPH_PCT](#) = 72U, [PBR_PERIPH_CMU](#), [PBR_PERIPH_MAX](#) }
pbr peripheral id

Functions

- void [Pbr_Hal_Init](#) (const [Pbr_CfgType](#) *Cfg)
Initialize pbr master and peripheral configuration.

4.4.1 Detailed Description

pbr hal define.

4.4.2 Enumeration Type Documentation

4.4.2.1 Pbr_MasterIdType

```
enum Pbr\_MasterIdType
```

pbr master id

Enumerator

PBR_MASTER_CORE	
PBR_MASTER_DEBUGGER	
PBR_MASTER_DMA	
PBR_MASTER_MAX	

Definition at line 56 of file Pbr_Hal.h.

4.4.2.2 Pbr_PeriphIdType

enum [Pbr_PeriphIdType](#)

pbr peripheral id

Enumerator

PBR_PERIPH_PWM0	
PBR_PERIPH_PWM1	
PBR_PERIPH_PWM2	
PBR_PERIPH_PWM3	
PBR_PERIPH_PWM4	
PBR_PERIPH_PWM5	
PBR_PERIPH_CRC	
PBR_PERIPH_GPIO	
PBR_PERIPH_PDT0	
PBR_PERIPH_PDT1	
PBR_PERIPH_MPU	
PBR_PERIPH_EIM	
PBR_PERIPH_SMU	
PBR_PERIPH_CKGEN	
PBR_PERIPH_RESERVE33	
PBR_PERIPH_FLASH	
PBR_PERIPH_ADC0	
PBR_PERIPH_ADC1	
PBR_PERIPH_ACMP0	
PBR_PERIPH_SPM	
PBR_PERIPH_RTC	
PBR_PERIPH_EIO	
PBR_PERIPH_WDG	
PBR_PERIPH_EWDG	
PBR_PERIPH_SPI0	
PBR_PERIPH_SPI1	
PBR_PERIPH_SPI2	
PBR_PERIPH_I2C0	
PBR_PERIPH_TIMER	
PBR_PERIPH_DMA	
PBR_PERIPH_UART0	
PBR_PERIPH_UART1	
PBR_PERIPH_UART2	
PBR_PERIPH_UART3	
PBR_PERIPH_PCT	
PBR_PERIPH_CMU	
PBR_PERIPH_MAX	

Definition at line 70 of file Pbr_Hal.h.

4.4.3 Function Documentation

4.4.3.1 Pbr_Hal_Init()

```
void Pbr_Hal_Init (
    const Pbr_CfgType * Cfg )
```

Initialize pbr master and peripheral configuration.

Note

Function ID: DES_MCL_API_301

Parameters

in	<i>Cfg</i>	the pointer to the Pbr_CfgType structure
----	------------	--

Returns

void

Definition at line 64 of file Pbr_Hal.c.

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