

AC784xx_DFP WDG

5.1.0

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Contents

1	Class Index	1
1.1	Class List	1
2	File Index	2
2.1	File List	2
3	Class Documentation	3
3.1	Wdg_HalConfigType Struct Reference	3
3.1.1	Detailed Description	3
3.1.2	Member Data Documentation	3
3.1.2.1	ClockSource	3
3.1.2.2	Config	4
3.1.2.3	TimeoutValue	4
3.1.2.4	WindowValue	4
4	File Documentation	5
4.1	AC784xx_API_Reference_Manual_WDG.pdf File Reference	5
4.2	AC784xx_Wdg_Reg.h File Reference	5
4.2.1	Detailed Description	6
4.2.2	Macro Definition Documentation	6
4.2.2.1	WDG_CNT_TRIGGER1	6
4.2.2.2	WDG_CNT_TRIGGER2	6
4.2.2.3	WDG_CNT_UNLOCK1	7
4.2.2.4	WDG_CNT_UNLOCK2	7
4.2.2.5	WDG_CS0_DBG_DISABLE	7
4.2.2.6	WDG_CS0_DBG_ENABLE	7

4.2.2.7	WDG_CS0_EN_DISABLE	7
4.2.2.8	WDG_CS0_EN_ENABLE	7
4.2.2.9	WDG_CS0_INT_DISABLE	8
4.2.2.10	WDG_CS0_INT_ENABLE	8
4.2.2.11	WDG_CS0_STOP_DISABLE	8
4.2.2.12	WDG_CS0_STOP_ENABLE	8
4.2.2.13	WDG_CS0_TST_DISABLE	8
4.2.2.14	WDG_CS0_TST_ENABLE	8
4.2.2.15	WDG_CS0_UPDATE_DISABLE	9
4.2.2.16	WDG_CS0_UPDATE_ENABLE	9
4.2.2.17	WDG_CS1_BUS_CLK	9
4.2.2.18	WDG_CS1_FLG_ENABLE	9
4.2.2.19	WDG_CS1_HSE_CLK	9
4.2.2.20	WDG_CS1_HSI_CLK	9
4.2.2.21	WDG_CS1_LSI_CLK	10
4.2.2.22	WDG_CS1_PRES_ENABLE	10
4.2.2.23	WDG_CS1_WIN_ENABLE	10
4.2.2.24	WDG_MODULE	10
4.2.2.25	WDG_PRESCALER_VALUE	10
4.2.2.26	WDG_TICKS_INIT_VALUE	10
4.2.2.27	WDOG_MIN_VALUE_TIMEOUT_U32	11
4.2.3	Function Documentation	11
4.2.3.1	Wdg_Reg_ReadCNT()	11
4.2.3.2	Wdg_Reg_ReadCS0()	11
4.2.3.3	Wdg_Reg_ReadCS1()	12
4.2.3.4	Wdg_Reg_ReadTOVAL()	12
4.2.3.5	Wdg_Reg_ReadWIN()	12
4.2.3.6	Wdg_Reg_WriteCNT()	12
4.2.3.7	Wdg_Reg_WriteCS0()	13
4.2.3.8	Wdg_Reg_WriteCS1()	13
4.2.3.9	Wdg_Reg_WriteTOVAL()	14

4.2.3.10	Wdg_Reg_WriteWIN()	14
4.3	Wdg_Hal.c File Reference	15
4.3.1	Detailed Description	15
4.3.2	Macro Definition Documentation	15
4.3.2.1	FREQUENCY_1KHZ	16
4.3.2.2	LSI_FREQUENCY	16
4.3.2.3	WDG_HAL_DELAY_TIME	16
4.3.2.4	WDG_HAL_MAX_TIMEOUT	16
4.3.2.5	WDG_PRESCALER_CLOCK_SHIFT	16
4.3.3	Function Documentation	16
4.3.3.1	ISR()	17
4.3.3.2	Wdg_Hal_DeInit()	17
4.3.3.3	Wdg_Hal_EnableInterrupt()	17
4.3.3.4	Wdg_Hal_Feed()	18
4.3.3.5	Wdg_Hal_GetFrequency()	18
4.3.3.6	Wdg_Hal_Init()	18
4.3.3.7	Wdg_Hal_InstallCallback()	19
4.4	Wdg_Hal.h File Reference	19
4.4.1	Detailed Description	20
4.4.2	Macro Definition Documentation	20
4.4.2.1	WDG_CONFIG_ALL	21
4.4.2.2	WDG_CONFIG_DBG_EN	21
4.4.2.3	WDG_CONFIG_FAST_TEST	21
4.4.2.4	WDG_CONFIG_LP_EN	21
4.4.2.5	WDG_CONFIG_PRESCALER_EN	21
4.4.2.6	WDG_CONFIG_WIN_EN	22
4.4.3	Enumeration Type Documentation	22
4.4.3.1	Wdg_ClockSourceType	22
4.4.4	Function Documentation	22
4.4.4.1	Wdg_Hal_DeInit()	22
4.4.4.2	Wdg_Hal_EnableInterrupt()	23
4.4.4.3	Wdg_Hal_Feed()	23
4.4.4.4	Wdg_Hal_GetFrequency()	23
4.4.4.5	Wdg_Hal_Init()	24
4.4.4.6	Wdg_Hal_InstallCallback()	24

Chapter 1

Class Index

1.1 Class List

Here are the classes, structs, unions and interfaces with brief descriptions:

Wdg_HalConfigType	Defines the configuration structure for WDG HAL	3
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Chapter 2

File Index

2.1 File List

Here is a list of all files with brief descriptions:

AC784xx_API_Reference_Manual_WDG.pdf	5
AC784xx_Wdg_Reg.h	
This file provides definitions & interfaces for read/write WDG hardware registers	5
Wdg_Hal.c	
This file provides WDG HAL functions	15
Wdg_Hal.h	
This file provides extern WDG HAL API	19

Chapter 3

Class Documentation

3.1 Wdg_HalConfigType Struct Reference

Defines the configuration structure for WDG HAL.

```
#include <Wdg_Hal.h>
```

Public Attributes

- [Wdg_ClockSourceType](#) [ClockSource](#)
- uint32 [WindowValue](#)
- uint32 [TimeoutValue](#)
- uint8 [Config](#)

3.1.1 Detailed Description

Defines the configuration structure for WDG HAL.

Definition at line 89 of file Wdg_Hal.h.

3.1.2 Member Data Documentation

3.1.2.1 ClockSource

[Wdg_ClockSourceType](#) Wdg_HalConfigType::ClockSource

Clock Source Type

Definition at line 91 of file Wdg_Hal.h.

3.1.2.2 Config

```
uint8 Wdg_HalConfigType::Config
```

WDG configuration. It is combinations (bitwise OR) of WDG_CONFIG_xxx.

Definition at line 94 of file Wdg_Hal.h.

3.1.2.3 TimeoutValue

```
uint32 Wdg_HalConfigType::TimeoutValue
```

Timeout Value in ms

Definition at line 93 of file Wdg_Hal.h.

3.1.2.4 WindowValue

```
uint32 Wdg_HalConfigType::WindowValue
```

Window Value in ms

Definition at line 92 of file Wdg_Hal.h.

The documentation for this struct was generated from the following file:

- [Wdg_Hal.h](#)

Chapter 4

File Documentation

4.1 AC784xx_API_Reference_Manual_WDG.pdf File Reference

4.2 AC784xx_Wdg_Reg.h File Reference

This file provides definitions & interfaces for read/write WDG hardware registers.

```
#include "Device_Register.h"
```

Macros

- #define [WDG_MODULE](#) ((WDG_Type *) WDG_BASE)
- #define [WDG_CS0_EN_ENABLE](#) (0x00000080U)
- #define [WDG_CS0_EN_DISABLE](#) (0x00000000U)
- #define [WDG_CS0_DBG_ENABLE](#) (0x00000002U)
- #define [WDG_CS0_DBG_DISABLE](#) (0x00000000U)
- #define [WDG_CS0_STOP_ENABLE](#) (0x00000001U)
- #define [WDG_CS0_STOP_DISABLE](#) (0x00000000U)
- #define [WDG_CS0_UPDATE_ENABLE](#) (0x00000020U)
- #define [WDG_CS0_UPDATE_DISABLE](#) (0x00000000U)
- #define [WDG_CS0_TST_ENABLE](#) (0x00000010U)
- #define [WDG_CS0_TST_DISABLE](#) (0x00000000U)
- #define [WDG_CS0_INT_ENABLE](#) (0x00000040U)
- #define [WDG_CS0_INT_DISABLE](#) (0x00000000U)
- #define [WDG_CS1_LSI_CLK](#) (0x00000000U)
- #define [WDG_CS1_BUS_CLK](#) (0x00000001U)
- #define [WDG_CS1_HSE_CLK](#) (0x00000003U)
- #define [WDG_CS1_HSI_CLK](#) (0x00000002U)
- #define [WDG_CS1_WIN_ENABLE](#) (0x00000080U)
- #define [WDG_CS1_FLG_ENABLE](#) (0x00000040U)
- #define [WDG_CS1_PRES_ENABLE](#) (0x00000010U)
- #define [WDG_CNT_UNLOCK1](#) (0xE064D987U)
- #define [WDG_CNT_UNLOCK2](#) (0x868A8478U)
- #define [WDG_CNT_TRIGGER1](#) (0x7908AD15U)
- #define [WDG_CNT_TRIGGER2](#) (0x5AD5A879U)
- #define [WDG_PRESCALER_VALUE](#) (0x00000100U)
- #define [WDG_TICKS_INIT_VALUE](#) (0x00000400U)
- #define [WDOG_MIN_VALUE_TIMEOUT_U32](#) (0x00000100U)

Functions

- LOCAL_INLINE uint32 [Wdg_Reg_ReadCNT](#) (void)
Read value of CNT register.
- LOCAL_INLINE void [Wdg_Reg_WriteCNT](#) (uint32 Value)
Set WDG CNT register.
- LOCAL_INLINE uint32 [Wdg_Reg_ReadTOVAL](#) (void)
Read value of TOVAL register.
- LOCAL_INLINE void [Wdg_Reg_WriteTOVAL](#) (uint32 Value)
Set WDG TOVAL register.
- LOCAL_INLINE uint32 [Wdg_Reg_ReadCS0](#) (void)
Read value of CS0 register.
- LOCAL_INLINE void [Wdg_Reg_WriteCS0](#) (uint32 Value)
Set WDG CS0 register.
- LOCAL_INLINE uint32 [Wdg_Reg_ReadCS1](#) (void)
Read value of CS1 register.
- LOCAL_INLINE void [Wdg_Reg_WriteCS1](#) (uint32 Value)
Set WDG CS1 register.
- LOCAL_INLINE uint32 [Wdg_Reg_ReadWIN](#) (void)
Read value of WIN register.
- LOCAL_INLINE void [Wdg_Reg_WriteWIN](#) (uint32 Value)
Set WDG WIN register.

4.2.1 Detailed Description

This file provides definitions & interfaces for read/write WDG hardware registers.

4.2.2 Macro Definition Documentation

4.2.2.1 WDG_CNT_TRIGGER1

```
#define WDG_CNT_TRIGGER1 (0x7908AD15U)
```

Definition at line 76 of file AC784xx_Wdg_Reg.h.

4.2.2.2 WDG_CNT_TRIGGER2

```
#define WDG_CNT_TRIGGER2 (0x5AD5A879U)
```

Definition at line 77 of file AC784xx_Wdg_Reg.h.

4.2.2.3 WDG_CNT_UNLOCK1

```
#define WDG_CNT_UNLOCK1 (0xE064D987U)
```

Definition at line 74 of file AC784xx_Wdg_Reg.h.

4.2.2.4 WDG_CNT_UNLOCK2

```
#define WDG_CNT_UNLOCK2 (0x868A8478U)
```

Definition at line 75 of file AC784xx_Wdg_Reg.h.

4.2.2.5 WDG_CS0_DBG_DISABLE

```
#define WDG_CS0_DBG_DISABLE (0x00000000U)
```

Definition at line 58 of file AC784xx_Wdg_Reg.h.

4.2.2.6 WDG_CS0_DBG_ENABLE

```
#define WDG_CS0_DBG_ENABLE (0x00000002U)
```

Definition at line 57 of file AC784xx_Wdg_Reg.h.

4.2.2.7 WDG_CS0_EN_DISABLE

```
#define WDG_CS0_EN_DISABLE (0x00000000U)
```

Definition at line 56 of file AC784xx_Wdg_Reg.h.

4.2.2.8 WDG_CS0_EN_ENABLE

```
#define WDG_CS0_EN_ENABLE (0x00000080U)
```

Definition at line 55 of file AC784xx_Wdg_Reg.h.

4.2.2.9 WDG_CS0_INT_DISABLE

```
#define WDG_CS0_INT_DISABLE (0x00000000U)
```

Definition at line 66 of file AC784xx_Wdg_Reg.h.

4.2.2.10 WDG_CS0_INT_ENABLE

```
#define WDG_CS0_INT_ENABLE (0x00000040U)
```

Definition at line 65 of file AC784xx_Wdg_Reg.h.

4.2.2.11 WDG_CS0_STOP_DISABLE

```
#define WDG_CS0_STOP_DISABLE (0x00000000U)
```

Definition at line 60 of file AC784xx_Wdg_Reg.h.

4.2.2.12 WDG_CS0_STOP_ENABLE

```
#define WDG_CS0_STOP_ENABLE (0x00000001U)
```

Definition at line 59 of file AC784xx_Wdg_Reg.h.

4.2.2.13 WDG_CS0_TST_DISABLE

```
#define WDG_CS0_TST_DISABLE (0x00000000U)
```

Definition at line 64 of file AC784xx_Wdg_Reg.h.

4.2.2.14 WDG_CS0_TST_ENABLE

```
#define WDG_CS0_TST_ENABLE (0x00000010U)
```

Definition at line 63 of file AC784xx_Wdg_Reg.h.

4.2.2.15 WDG_CS0_UPDATE_DISABLE

```
#define WDG_CS0_UPDATE_DISABLE (0x00000000U)
```

Definition at line 62 of file AC784xx_Wdg_Reg.h.

4.2.2.16 WDG_CS0_UPDATE_ENABLE

```
#define WDG_CS0_UPDATE_ENABLE (0x00000020U)
```

Definition at line 61 of file AC784xx_Wdg_Reg.h.

4.2.2.17 WDG_CS1_BUS_CLK

```
#define WDG_CS1_BUS_CLK (0x00000001U)
```

Definition at line 68 of file AC784xx_Wdg_Reg.h.

4.2.2.18 WDG_CS1_FLG_ENABLE

```
#define WDG_CS1_FLG_ENABLE (0x00000040U)
```

Definition at line 72 of file AC784xx_Wdg_Reg.h.

4.2.2.19 WDG_CS1_HSE_CLK

```
#define WDG_CS1_HSE_CLK (0x00000003U)
```

Definition at line 69 of file AC784xx_Wdg_Reg.h.

4.2.2.20 WDG_CS1_HSI_CLK

```
#define WDG_CS1_HSI_CLK (0x00000002U)
```

Definition at line 70 of file AC784xx_Wdg_Reg.h.

4.2.2.21 WDG_CS1_LSI_CLK

```
#define WDG_CS1_LSI_CLK (0x00000000U)
```

Definition at line 67 of file AC784xx_Wdg_Reg.h.

4.2.2.22 WDG_CS1_PRES_ENABLE

```
#define WDG_CS1_PRES_ENABLE (0x00000010U)
```

Definition at line 73 of file AC784xx_Wdg_Reg.h.

4.2.2.23 WDG_CS1_WIN_ENABLE

```
#define WDG_CS1_WIN_ENABLE (0x00000080U)
```

Definition at line 71 of file AC784xx_Wdg_Reg.h.

4.2.2.24 WDG_MODULE

```
#define WDG_MODULE ((WDG_Type *) WDG_BASE)
```

Definition at line 53 of file AC784xx_Wdg_Reg.h.

4.2.2.25 WDG_PRESCALER_VALUE

```
#define WDG_PRESCALER_VALUE (0x00000100U)
```

Definition at line 78 of file AC784xx_Wdg_Reg.h.

4.2.2.26 WDG_TICKS_INIT_VALUE

```
#define WDG_TICKS_INIT_VALUE (0x00000400U)
```

Definition at line 79 of file AC784xx_Wdg_Reg.h.

4.2.2.27 WDOG_MIN_VALUE_TIMEOUT_U32

```
#define WDOG_MIN_VALUE_TIMEOUT_U32 (0x00000100U)
```

Definition at line 80 of file AC784xx_Wdg_Reg.h.

4.2.3 Function Documentation

4.2.3.1 Wdg_Reg_ReadCNT()

```
LOCAL_INLINE uint32 Wdg_Reg_ReadCNT (  
    void )
```

Read value of CNT register.

Note

Function ID: DES_WDG_API_50

Returns

value of CNT register

Definition at line 92 of file AC784xx_Wdg_Reg.h.

4.2.3.2 Wdg_Reg_ReadCS0()

```
LOCAL_INLINE uint32 Wdg_Reg_ReadCS0 (  
    void )
```

Read value of CS0 register.

Note

Function ID: DES_WDG_API_54

Returns

value of CS0 register

Definition at line 138 of file AC784xx_Wdg_Reg.h.

4.2.3.3 Wdg_Reg_ReadCS1()

```
LOCAL_INLINE uint32 Wdg_Reg_ReadCS1 (  
    void )
```

Read value of CS1 register.

Note

Function ID: DES_WDG_API_56

Returns

value of CS1 register

Definition at line 161 of file AC784xx_Wdg_Reg.h.

4.2.3.4 Wdg_Reg_ReadTOVAL()

```
LOCAL_INLINE uint32 Wdg_Reg_ReadTOVAL (  
    void )
```

Read value of TOVAL register.

Note

Function ID: DES_WDG_API_52

Returns

value of TOVAL register

Definition at line 115 of file AC784xx_Wdg_Reg.h.

4.2.3.5 Wdg_Reg_ReadWIN()

```
LOCAL_INLINE uint32 Wdg_Reg_ReadWIN (  
    void )
```

Read value of WIN register.

Note

Function ID: DES_WDG_API_58

Returns

value of WIN register

Definition at line 184 of file AC784xx_Wdg_Reg.h.

4.2.3.6 Wdg_Reg_WriteCNT()

```
LOCAL_INLINE void Wdg_Reg_WriteCNT (  
    uint32 Value )
```

Set WDG CNT register.

Note

Function ID: DES_WDG_API_51

Parameters

<i>in</i>	<i>Value</i>	Value set to CNT register
-----------	--------------	---------------------------

Returns

none

Definition at line 104 of file AC784xx_Wdg_Reg.h.

4.2.3.7 Wdg_Reg_WriteCS0()

```
LOCAL_INLINE void Wdg_Reg_WriteCS0 (  
    uint32 Value )
```

Set WDG CS0 register.

Note

Function ID: DES_WDG_API_55

Parameters

<i>in</i>	<i>Value</i>	Value set to CS0 register
-----------	--------------	---------------------------

Returns

none

Definition at line 150 of file AC784xx_Wdg_Reg.h.

4.2.3.8 Wdg_Reg_WriteCS1()

```
LOCAL_INLINE void Wdg_Reg_WriteCS1 (  
    uint32 Value )
```

Set WDG CS1 register.

Note

Function ID: DES_WDG_API_57

Parameters

<i>in</i>	<i>Value</i>	Value set to CS1 register
-----------	--------------	---------------------------

Returns

none

Definition at line 173 of file AC784xx_Wdg_Reg.h.

4.2.3.9 Wdg_Reg_WriteTOVAL()

```
LOCAL_INLINE void Wdg_Reg_WriteTOVAL (
    uint32 Value )
```

Set WDG TOVAL register.

Note

Function ID: DES_WDG_API_53

Parameters

in	<i>Value</i>	Value set to TOVAL register
----	--------------	-----------------------------

Returns

none

Definition at line 127 of file AC784xx_Wdg_Reg.h.

4.2.3.10 Wdg_Reg_WriteWIN()

```
LOCAL_INLINE void Wdg_Reg_WriteWIN (
    uint32 Value )
```

Set WDG WIN register.

Note

Function ID: DES_WDG_API_59

Parameters

in	<i>Value</i>	Value set to WIN register
----	--------------	---------------------------

Returns

none

Definition at line 196 of file AC784xx_Wdg_Reg.h.

4.3 Wdg_Hal.c File Reference

This file provides WDG HAL functions.

```
#include "Wdg_Hal.h"
#include "AC784xx_Wdg_Reg.h"
#include "Ckgen_Hal.h"
#include "OsIf_Critical.h"
#include "OsIf_Time.h"
#include "Rcm_Hal.h"
#include "Core_Hal.h"
```

Macros

- #define [WDG_PRESCALER_CLOCK_SHIFT](#) 0x08U
Define shift number for tranformation between clock tick and time.
- #define [WDG_HAL_MAX_TIMEOUT](#) 0xFFFFU
Max value of WDG timeout register.
- #define [LSI_FREQUENCY](#) 128U /* 128KHz / 1000*/
Frequency of LSI.
- #define [FREQUENCY_1KHZ](#) (1000U)
For frequency transformation KHz <-> Hz.
- #define [WDG_HAL_DELAY_TIME](#) (50U)
Delay time in microsecond for clock stable.

Functions

- void [Wdg_Hal_Init](#) (const [Wdg_HalConfigType](#) *ConfigPtr)
Initializes WDG hardware.
- void [Wdg_Hal_DeInit](#) (void)
De-initializes (disable) WDG hardware.
- void [Wdg_Hal_Feed](#) (void)
Feed WDG hardware. Its counter will be reset.
- void [Wdg_Hal_InstallCallback](#) (const [Hal_CallbackType](#) Func, void *Args)
Install WDG interrupt callback function.
- [ISR](#) ([WDG_IRQHandler](#))
- uint32 [Wdg_Hal_GetFrequency](#) ([Wdg_ClockSourceType](#) ClockSource)
Get WDG frequency of Clock source in KHz.
- void [Wdg_Hal_EnableInterrupt](#) (boolean Enable)
Enable WDG interrupt.

4.3.1 Detailed Description

This file provides WDG HAL functions.

4.3.2 Macro Definition Documentation

4.3.2.1 FREQUENCY_1KHZ

```
#define FREQUENCY_1KHZ (1000U)
```

For frequency transformation KHz <->Hz.

Definition at line 61 of file Wdg_Hal.c.

4.3.2.2 LSI_FREQUENCY

```
#define LSI_FREQUENCY 128U /* 128KHz / 1000*/
```

Frequency of LSI.

Definition at line 58 of file Wdg_Hal.c.

4.3.2.3 WDG_HAL_DELAY_TIME

```
#define WDG_HAL_DELAY_TIME (50U)
```

Delay time in microsecond for clock stable.

Definition at line 64 of file Wdg_Hal.c.

4.3.2.4 WDG_HAL_MAX_TIMEOUT

```
#define WDG_HAL_MAX_TIMEOUT 0xFFFFU
```

Max value of WDG timeout register.

Definition at line 55 of file Wdg_Hal.c.

4.3.2.5 WDG_PRESCALER_CLOCK_SHIFT

```
#define WDG_PRESCALER_CLOCK_SHIFT 0x08U
```

Define shift number for tranformation between clock tick and time.

Definition at line 52 of file Wdg_Hal.c.

4.3.3 Function Documentation

4.3.3.1 ISR()

```
ISR (
    WDG_IRQHandler )
```

Definition at line 194 of file Wdg_Hal.c.

4.3.3.2 Wdg_Hal_DeInit()

```
void Wdg_Hal_DeInit (
    void )
```

De-initializes (disable) WDG hardware.

Note

Function ID: DES_WDG_API_202

Returns

void

Definition at line 151 of file Wdg_Hal.c.

4.3.3.3 Wdg_Hal_EnableInterrupt()

```
void Wdg_Hal_EnableInterrupt (
    boolean Enable )
```

Enable WDG interrupt.

Note

Function ID: DES_WDG_API_207
Service ID: none

Parameters

in	<i>Enable</i>	TRUE: Enable WDG interrupt, FALSE(0): Disable WDG interrupt.
----	---------------	--

Returns

void

Definition at line 337 of file Wdg_Hal.c.

4.3.3.4 Wdg_Hal_Feed()

```
void Wdg_Hal_Feed (
    void )
```

Feed WDG hardware. Its counter will be reset.

Note

Function ID: DES_WDG_API_203

Returns

void

Definition at line 179 of file Wdg_Hal.c.

4.3.3.5 Wdg_Hal_GetFrequency()

```
uint32 Wdg_Hal_GetFrequency (
    Wdg_ClockSourceType ClockSource )
```

Get WDG frequency of Clock source in KHz.

Note

Function ID: DES_WDG_API_206

Parameters

in	<i>ClockSource</i>	ClockSource of WDG
----	--------------------	--------------------

Returns

The frequency(in KHz) of WDG.

See also

[Wdg_ClockSourceType](#)

Definition at line 222 of file Wdg_Hal.c.

4.3.3.6 Wdg_Hal_Init()

```
void Wdg_Hal_Init (
    const Wdg_HalConfigType * ConfigPtr )
```

Initializes WDG hardware.

Note

Function ID: DES_WDG_API_201

Parameters

in	<i>ConfigPtr</i>	: Pointer to WDG hardware setting. It can't be NULL_PTR.
----	------------------	--

Returns

void

See also

struct [Wdg_HalConfigType](#)

Definition at line 100 of file Wdg_Hal.c.

4.3.3.7 Wdg_Hal_InstallCallback()

```
void Wdg_Hal_InstallCallback (
    const Hal_CallbackType Func,
    void * Args )
```

Install WDG interrupt callback function.

Note

Function ID: DES_WDG_API_204

Parameters

in	<i>Func</i>	The pointer to callback function to be installed
in	<i>Args</i>	The parameter of the callback function

Returns

void

Definition at line 187 of file Wdg_Hal.c.

4.4 Wdg_Hal.h File Reference

This file provides extern WDG HAL API.

```
#include "Device_Register.h"
```

Classes

- struct [Wdg_HalConfigType](#)
Defines the configuration structure for WDG HAL.

Macros

- #define [WDG_CONFIG_PRESCALER_EN](#) 0x01U
Prescaler enable config for WDG clock.
- #define [WDG_CONFIG_DBG_EN](#) 0x02U
Enable WDG in Debug mode.
- #define [WDG_CONFIG_FAST_TEST](#) 0x04U
Enable WDG fast test mode.
- #define [WDG_CONFIG_LP_EN](#) 0x08U
Enable WDG in Low Power mode.
- #define [WDG_CONFIG_WIN_EN](#) 0x10U
Enable WDG window mode.
- #define [WDG_CONFIG_ALL](#)
Enable all configuraiton of WDG.

Enumerations

- enum [Wdg_ClockSourceType](#) {
 [WDG_CLOCK_LSI](#) = 0U, [WDG_CLOCK_HSI](#), [WDG_CLOCK_HSE](#), [WDG_CLOCK_BUS](#),
 [WDG_CLOCK_INVALID](#) }
WDG Clock source type.

Functions

- void [Wdg_Hal_Init](#) (const [Wdg_HalConfigType](#) *ConfigPtr)
Initializes WDG hardware.
- void [Wdg_Hal_DeInit](#) (void)
De-initializes (disable) WDG hardware.
- void [Wdg_Hal_Feed](#) (void)
Feed WDG hardware. Its counter will be reset.
- void [Wdg_Hal_InstallCallback](#) (const [Hal_CallbackType](#) Func, void *Args)
Install WDG interrupt callback function.
- uint32 [Wdg_Hal_GetFrequency](#) ([Wdg_ClockSourceType](#) ClockSource)
Get WDG frequency of Clock source in KHz.
- void [Wdg_Hal_EnableInterrupt](#) (boolean Enable)
Enable WDG interrupt.

4.4.1 Detailed Description

This file provides extern WDG HAL API.

4.4.2 Macro Definition Documentation

4.4.2.1 WDG_CONFIG_ALL

```
#define WDG_CONFIG_ALL
```

Value:

```
(WDG_CONFIG_PRESCALER_EN | WDG_CONFIG_DBG_EN | \
    WDG_CONFIG_FAST_TEST | WDG_CONFIG_LP_EN | \
    WDG_CONFIG_WIN_EN)
```

Enable all configuraiton of WDG.

Definition at line 83 of file Wdg_Hal.h.

4.4.2.2 WDG_CONFIG_DBG_EN

```
#define WDG_CONFIG_DBG_EN 0x02U
```

Enable WDG in Debug mode.

Definition at line 71 of file Wdg_Hal.h.

4.4.2.3 WDG_CONFIG_FAST_TEST

```
#define WDG_CONFIG_FAST_TEST 0x04U
```

Enable WDG fast test mode.

Definition at line 74 of file Wdg_Hal.h.

4.4.2.4 WDG_CONFIG_LP_EN

```
#define WDG_CONFIG_LP_EN 0x08U
```

Enable WDG in Low Power mode.

Definition at line 77 of file Wdg_Hal.h.

4.4.2.5 WDG_CONFIG_PRESCALER_EN

```
#define WDG_CONFIG_PRESCALER_EN 0x01U
```

Prescaler enable config for WDG clock.

Definition at line 68 of file Wdg_Hal.h.

4.4.2.6 WDG_CONFIG_WIN_EN

```
#define WDG_CONFIG_WIN_EN 0x10U
```

Enable WDG window mode.

Definition at line 80 of file Wdg_Hal.h.

4.4.3 Enumeration Type Documentation

4.4.3.1 Wdg_ClockSourceType

```
enum Wdg_ClockSourceType
```

WDG Clock source type.

Enumerator

WDG_CLOCK_LSI	WDG uses LSI as clock source.
WDG_CLOCK_HSI	WDG uses HSI as clock source.
WDG_CLOCK_HSE	WDG uses HSE as clock source.
WDG_CLOCK_BUS	WDG uses Bus Clock as clock source.
WDG_CLOCK_INVALID	Invalid clock source.

Definition at line 58 of file Wdg_Hal.h.

4.4.4 Function Documentation

4.4.4.1 Wdg_Hal_DeInit()

```
void Wdg_Hal_DeInit (  
    void )
```

De-initializes (disable) WDG hardware.

Note

Function ID: DES_WDG_API_202

Returns

void

Definition at line 151 of file Wdg_Hal.c.

4.4.4.2 Wdg_Hal_EnableInterrupt()

```
void Wdg_Hal_EnableInterrupt (
    boolean Enable )
```

Enable WDG interrupt.

Note

Function ID: DES_WDG_API_207
Service ID: none

Parameters

in	<i>Enable</i>	TRUE: Enable WDG interrupt, FALSE(0): Disable WDG interrupt.
----	---------------	--

Returns

void

Definition at line 337 of file Wdg_Hal.c.

4.4.4.3 Wdg_Hal_Feed()

```
void Wdg_Hal_Feed (
    void )
```

Feed WDG hardware. Its counter will be reset.

Note

Function ID: DES_WDG_API_203

Returns

void

Definition at line 179 of file Wdg_Hal.c.

4.4.4.4 Wdg_Hal_GetFrequency()

```
uint32 Wdg_Hal_GetFrequency (
    Wdg_ClockSourceType ClockSource )
```

Get WDG frequency of Clock source in KHz.

Note

Function ID: DES_WDG_API_206

Parameters

in	<i>ClockSource</i>	ClockSource of WDG
----	--------------------	--------------------

Returns

The frequency(in KHz) of WDG.

See also

[Wdg_ClockSourceType](#)

Definition at line 222 of file Wdg_Hal.c.

4.4.4.5 Wdg_Hal_Init()

```
void Wdg_Hal_Init (
    const Wdg\_HalConfigType * ConfigPtr )
```

Initializes WDG hardware.

Note

Function ID: DES_WDG_API_201

Parameters

in	<i>ConfigPtr</i>	: Pointer to WDG hardware setting. It can't be NULL_PTR.
----	------------------	--

Returns

void

See also

struct [Wdg_HalConfigType](#)

Definition at line 100 of file Wdg_Hal.c.

4.4.4.6 Wdg_Hal_InstallCallback()

```
void Wdg_Hal_InstallCallback (
    const Hal_CallbackType Func,
    void * Args )
```

Install WDG interrupt callback function.

Note

Function ID: DES_WDG_API_204

Parameters

in	<i>Func</i>	The pointer to callback function to be installed
in	<i>Args</i>	The parameter of the callback function

Returns

void

Definition at line 187 of file Wdg_Hal.c.

Index

AC784xx_API_Reference_Manual_WDG.pdf, [5](#)

AC784xx_Wdg_Reg.h, [5](#)

WDG_CNT_TRIGGER1, [6](#)

WDG_CNT_TRIGGER2, [6](#)

WDG_CNT_UNLOCK1, [6](#)

WDG_CNT_UNLOCK2, [7](#)

WDG_CS0_DBG_DISABLE, [7](#)

WDG_CS0_DBG_ENABLE, [7](#)

WDG_CS0_EN_DISABLE, [7](#)

WDG_CS0_EN_ENABLE, [7](#)

WDG_CS0_INT_DISABLE, [7](#)

WDG_CS0_INT_ENABLE, [8](#)

WDG_CS0_STOP_DISABLE, [8](#)

WDG_CS0_STOP_ENABLE, [8](#)

WDG_CS0_TST_DISABLE, [8](#)

WDG_CS0_TST_ENABLE, [8](#)

WDG_CS0_UPDATE_DISABLE, [8](#)

WDG_CS0_UPDATE_ENABLE, [9](#)

WDG_CS1_BUS_CLK, [9](#)

WDG_CS1_FLG_ENABLE, [9](#)

WDG_CS1_HSE_CLK, [9](#)

WDG_CS1_HSI_CLK, [9](#)

WDG_CS1_LSI_CLK, [9](#)

WDG_CS1_PRESC_ENABLE, [10](#)

WDG_CS1_WIN_ENABLE, [10](#)

WDG_MODULE, [10](#)

WDG_PRESCALER_VALUE, [10](#)

WDG_TICKS_INIT_VALUE, [10](#)

WDOG_MIN_VALUE_TIMEOUT_U32, [10](#)

Wdg_Reg_ReadCNT, [11](#)

Wdg_Reg_ReadCS0, [11](#)

Wdg_Reg_ReadCS1, [11](#)

Wdg_Reg_ReadTOVAL, [12](#)

Wdg_Reg_ReadWIN, [12](#)

Wdg_Reg_WriteCNT, [12](#)

Wdg_Reg_WriteCS0, [13](#)

Wdg_Reg_WriteCS1, [13](#)

Wdg_Reg_WriteTOVAL, [14](#)

Wdg_Reg_WriteWIN, [14](#)

ClockSource

Wdg_HalConfigType, [3](#)

Config

Wdg_HalConfigType, [3](#)

FREQUENCY_1KHZ

Wdg_Hal.c, [15](#)

ISR

Wdg_Hal.c, [16](#)

LSI_FREQUENCY

Wdg_Hal.c, [16](#)

TimeoutValue

Wdg_HalConfigType, [4](#)

WDG_CNT_TRIGGER1

AC784xx_Wdg_Reg.h, [6](#)

WDG_CNT_TRIGGER2

AC784xx_Wdg_Reg.h, [6](#)

WDG_CNT_UNLOCK1

AC784xx_Wdg_Reg.h, [6](#)

WDG_CNT_UNLOCK2

AC784xx_Wdg_Reg.h, [7](#)

WDG_CONFIG_ALL

Wdg_Hal.h, [20](#)

WDG_CONFIG_DBG_EN

Wdg_Hal.h, [21](#)

WDG_CONFIG_FAST_TEST

Wdg_Hal.h, [21](#)

WDG_CONFIG_LP_EN

Wdg_Hal.h, [21](#)

WDG_CONFIG_PRESCALER_EN

Wdg_Hal.h, [21](#)

WDG_CONFIG_WIN_EN

Wdg_Hal.h, [21](#)

WDG_CS0_DBG_DISABLE

AC784xx_Wdg_Reg.h, [7](#)

WDG_CS0_DBG_ENABLE

AC784xx_Wdg_Reg.h, [7](#)

WDG_CS0_EN_DISABLE

AC784xx_Wdg_Reg.h, [7](#)

WDG_CS0_EN_ENABLE

AC784xx_Wdg_Reg.h, [7](#)

WDG_CS0_INT_DISABLE

AC784xx_Wdg_Reg.h, [7](#)

WDG_CS0_INT_ENABLE

AC784xx_Wdg_Reg.h, [8](#)

WDG_CS0_STOP_DISABLE

AC784xx_Wdg_Reg.h, [8](#)

WDG_CS0_STOP_ENABLE

AC784xx_Wdg_Reg.h, [8](#)

WDG_CS0_TST_DISABLE

AC784xx_Wdg_Reg.h, [8](#)

WDG_CS0_TST_ENABLE

AC784xx_Wdg_Reg.h, [8](#)

WDG_CS0_UPDATE_DISABLE

AC784xx_Wdg_Reg.h, [8](#)

WDG_CS0_UPDATE_ENABLE

AC784xx_Wdg_Reg.h, [9](#)

WDG_CS1_BUS_CLK

AC784xx_Wdg_Reg.h, [9](#)

WDG_CS1_FLG_ENABLE

AC784xx_Wdg_Reg.h, [9](#)

WDG_CS1_HSE_CLK

AC784xx_Wdg_Reg.h, [9](#)
 WDG_CS1_HSI_CLK
 AC784xx_Wdg_Reg.h, [9](#)
 WDG_CS1_LSI_CLK
 AC784xx_Wdg_Reg.h, [9](#)
 WDG_CS1_PRES_ENABLE
 AC784xx_Wdg_Reg.h, [10](#)
 WDG_CS1_WIN_ENABLE
 AC784xx_Wdg_Reg.h, [10](#)
 WDG_HAL_DELAY_TIME
 Wdg_Hal.c, [16](#)
 WDG_HAL_MAX_TIMEOUT
 Wdg_Hal.c, [16](#)
 WDG_MODULE
 AC784xx_Wdg_Reg.h, [10](#)
 WDG_PRESCALER_CLOCK_SHIFT
 Wdg_Hal.c, [16](#)
 WDG_PRESCALER_VALUE
 AC784xx_Wdg_Reg.h, [10](#)
 WDG_TICKS_INIT_VALUE
 AC784xx_Wdg_Reg.h, [10](#)
 WDOG_MIN_VALUE_TIMEOUT_U32
 AC784xx_Wdg_Reg.h, [10](#)
 Wdg_ClockSourceType
 Wdg_Hal.h, [22](#)
 Wdg_Hal.c, [15](#)
 FREQUENCY_1KHZ, [15](#)
 ISR, [16](#)
 LSI_FREQUENCY, [16](#)
 WDG_HAL_DELAY_TIME, [16](#)
 WDG_HAL_MAX_TIMEOUT, [16](#)
 WDG_PRESCALER_CLOCK_SHIFT, [16](#)
 Wdg_Hal_DeInit, [17](#)
 Wdg_Hal_EnableInterrupt, [17](#)
 Wdg_Hal_Feed, [17](#)
 Wdg_Hal_GetFrequency, [18](#)
 Wdg_Hal_Init, [18](#)
 Wdg_Hal_InstallCallback, [19](#)
 Wdg_Hal.h, [19](#)
 WDG_CONFIG_ALL, [20](#)
 WDG_CONFIG_DBG_EN, [21](#)
 WDG_CONFIG_FAST_TEST, [21](#)
 WDG_CONFIG_LP_EN, [21](#)
 WDG_CONFIG_PRESCALER_EN, [21](#)
 WDG_CONFIG_WIN_EN, [21](#)
 Wdg_ClockSourceType, [22](#)
 Wdg_Hal_DeInit, [22](#)
 Wdg_Hal_EnableInterrupt, [22](#)
 Wdg_Hal_Feed, [23](#)
 Wdg_Hal_GetFrequency, [23](#)
 Wdg_Hal_Init, [24](#)
 Wdg_Hal_InstallCallback, [24](#)
 Wdg_Hal_DeInit
 Wdg_Hal.c, [17](#)
 Wdg_Hal.h, [22](#)
 Wdg_Hal_EnableInterrupt
 Wdg_Hal.c, [17](#)
 Wdg_Hal.h, [22](#)
 Wdg_Hal_Feed
 Wdg_Hal.c, [17](#)
 Wdg_Hal.h, [23](#)
 Wdg_Hal_GetFrequency
 Wdg_Hal.c, [18](#)
 Wdg_Hal.h, [23](#)
 Wdg_Hal_Init
 Wdg_Hal.c, [18](#)
 Wdg_Hal.h, [24](#)
 Wdg_Hal_InstallCallback
 Wdg_Hal.c, [19](#)
 Wdg_Hal.h, [24](#)
 Wdg_Hal_ReadCNT
 AC784xx_Wdg_Reg.h, [11](#)
 Wdg_Hal_ReadCS0
 AC784xx_Wdg_Reg.h, [11](#)
 Wdg_Hal_ReadCS1
 AC784xx_Wdg_Reg.h, [11](#)
 Wdg_Hal_ReadTOVAL
 AC784xx_Wdg_Reg.h, [12](#)
 Wdg_Hal_ReadWIN
 AC784xx_Wdg_Reg.h, [12](#)
 Wdg_Hal_WriteCNT
 AC784xx_Wdg_Reg.h, [12](#)
 Wdg_Hal_WriteCS0
 AC784xx_Wdg_Reg.h, [13](#)
 Wdg_Hal_WriteCS1
 AC784xx_Wdg_Reg.h, [13](#)
 Wdg_Hal_WriteTOVAL
 AC784xx_Wdg_Reg.h, [14](#)
 Wdg_Hal_WriteWIN
 AC784xx_Wdg_Reg.h, [14](#)
 WindowValue
 Wdg_HalConfigType, [4](#)